



Notes:

1. IBM RP3 augments the IBM RT/PC architecture with fetch-and-op functions.
2. Torrent TO supplements the MIPS II instruction set.
3. Specific instruction: ADD Rx, R15, 8, bit_immediate & 07. R15 is the PC. The 12 bit immediate is split into 8 bit immediate and 4 bit "position", which can specify any odd bit starting position. & 07 bit position starting bit 25 for 24-bit PC.
4. CPSR and CPYR are used on the system before the SETBIT and CLRBIT instructions to set single bits and clear single bits.
5. To perform FMIN, use FSUB f_a, f_a, fb, fa. To perform FMAX, use FSUB f_a, f_a, fb then FSEL f_a, f_a, fb. Registers f_a and f_b are the register contents being compared.
6. INT, TO_EXTENDED requires an EXTENDED, TO_SINGLE or EXTENDED, TO_DOUBLE to convert from integer to single or double and converting floating point to integer requires SINGLE, TO_EXTENDED or DOUBLE, TO_EXTENDED followed by EXTENDED, TO_INT.